PATENT NUMBER and ISSUE DATE

U.S. UTILITY Patent Application

*** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-394215 12/26/2001 **PG-PUB DO NOT PUBLISH	APPL NUM	FILING DATE	CLASS	SUBCLASS		/ /	EXAN	IINER					
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Verified and Acknowledged Examiners's initials 2.20212US2	35 USC 119 condit	ions met	⊐ ye										
TITLE: Semiconductor device and method of manufacturing the same U.S.DEPT. OF COMM./PAT.& TM-PTO-435L(Rev. 12-9	HILE: Semico	nauctor device an	na metnoc	or manuractu	inng the		EPT. OF COMM.	/PAT.& TI	J-PTO-435L	(Rev 12-94)			

NOTICE OF ALLO	WANCE MAILED	CLAIMS ALLOWED							
		Assistant Examiner	Total Claims Print Claim for O.G						
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Amount Due	Date Paid		Sheets Drwg. Figs.Drwg. Print Fig.						
		Primary Examiner	Applicati n Examiner						
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	DISCLAMER	WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.							
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